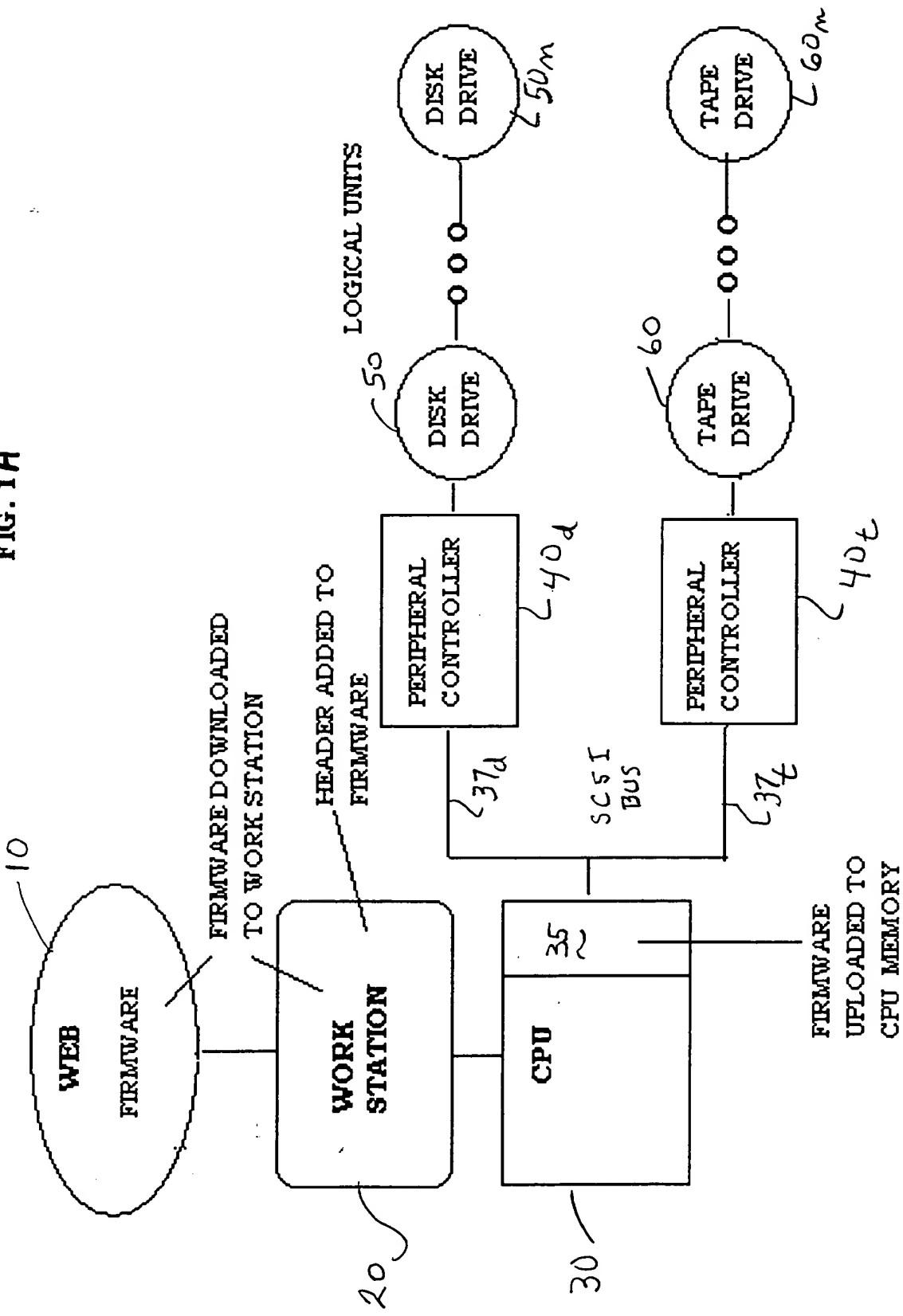


FIG 1A

DKT 041 - 468-L

FIG. 1A



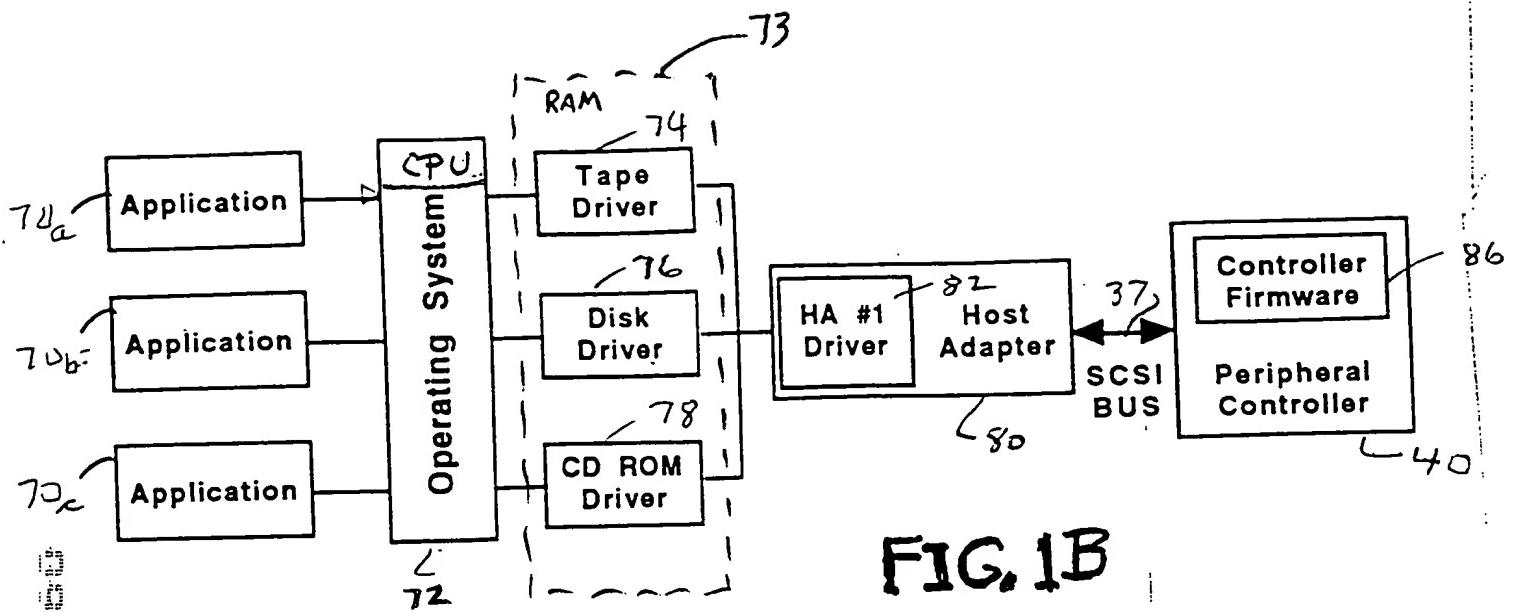
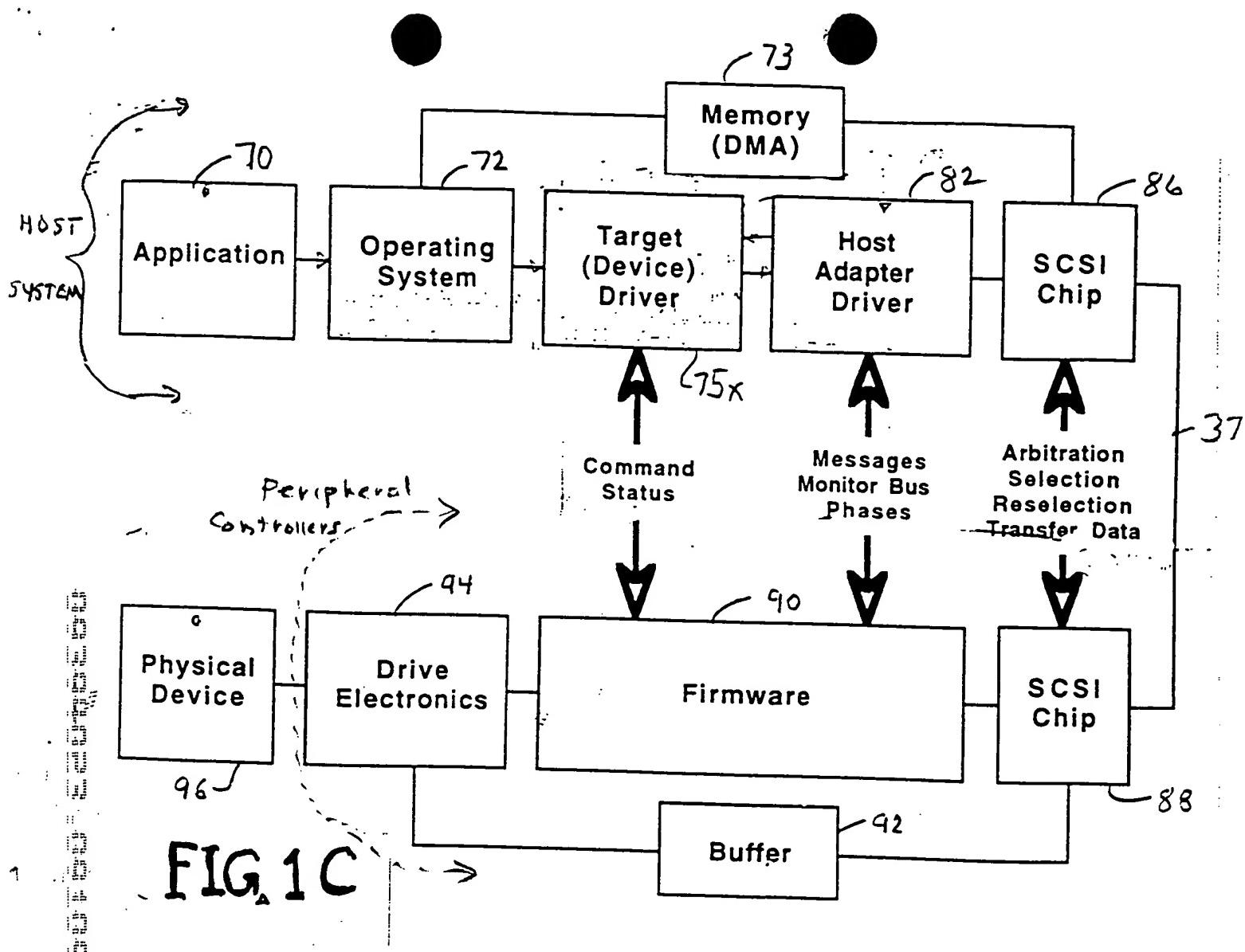
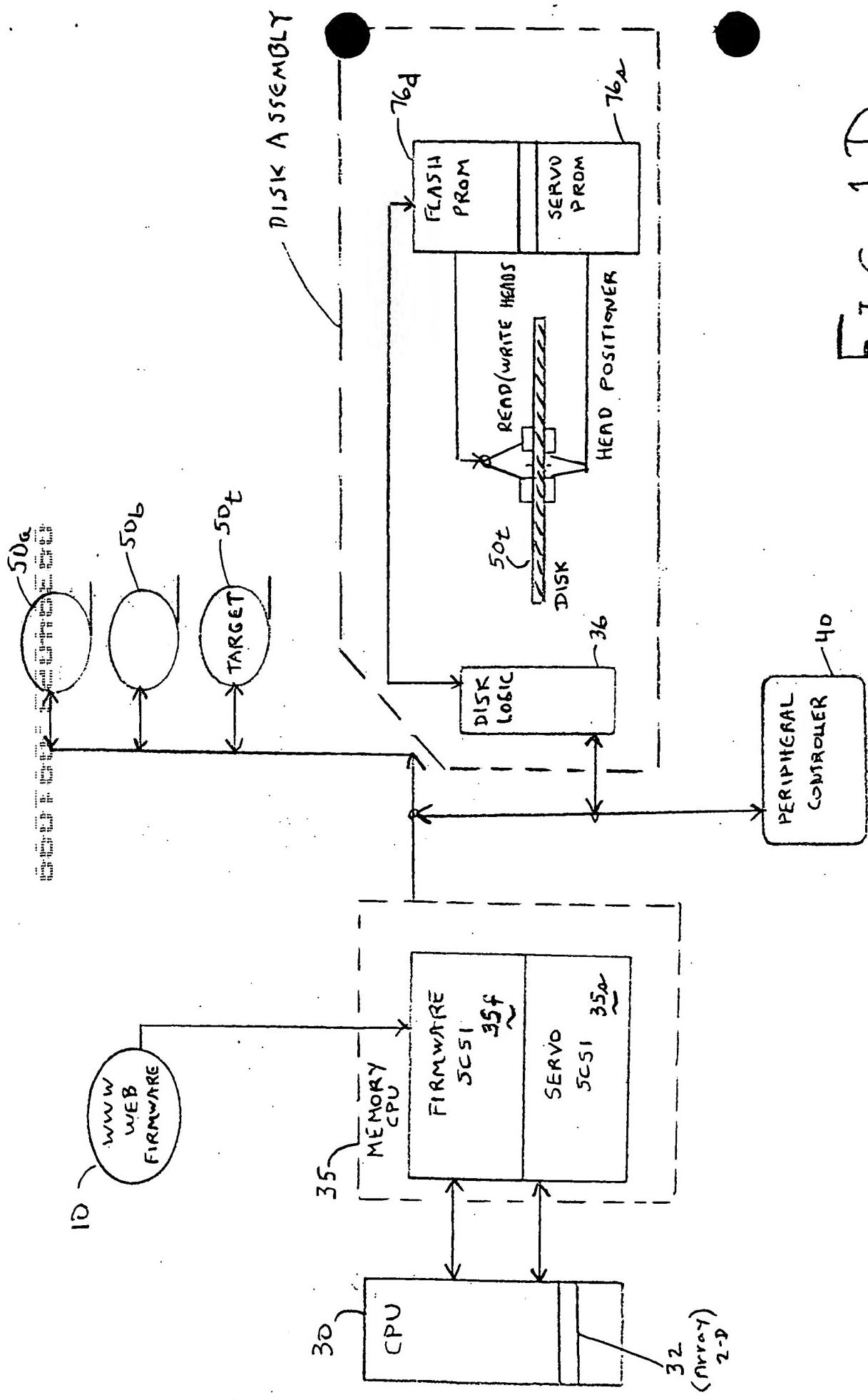


FIG. 1B



041-468-L
8/6/99

FIG 1D



Typical Twelve-Byte CDB

(Table 23)

	bit 7	bit 6	bit 5	bit 4	bit 3	bit 2	bit 1	bit 0				
byte 0	Operation Code											
byte 1	Logical Unit Number				Reserved							
byte 2	(MSB) LBA = Logical Block Address (if required)											
byte 3												
byte 4												
byte 5	(LSB)											
byte 6	(MSB) Transfer Length (if required)											
byte 7												
byte 8	Parameter List Length (if required)											
byte 9	Allocation Length (if required) (LSB)											
byte 10	Reserved											
byte 11	Control Byte											

FIG. 2

- WRITE BUFFER command

Bit Byte	7	6	5	4	3	2	1	0				
0	Operation code (3Bh)											
1	Reserved				Mode							
2	Buffer ID											
3	(MSB)											
4	Buffer offset											
5												
6	(MSB)											
7	Parameter list length											
6												
9	Control											

FIG. 3

WRITE BUFFER Mode field

Mode	Description	Implementation requirements
000b	Write combined header and data	Optional
001b	Vendor-specific	Vendor-specific
010b	Write data	Optional
011b	Reserved	Reserved
100b	Download microcode	Optional
101b	Download microcode and save	Optional
110b	Download microcode with offsets	Optional
111b	Download microcode with offsets and save	Optional

FIG. 4

OPERATION CODE

Bits	7	6	5	4	3	2	1	0
Byte 0			Group Code				Command Code	

Group Code Field

Group	bit 7	bit 6	bit 5	Number of Command Bytes
0	0	0	0	six.
1	0	0	1	ten
2	0	1	0	ten (new in SCSI-2)
3	0	1	1	reserved
4	1	0	0	reserved
5	1	0	1	twelve .
6	1	1	0	vendor Specific
7	1	1	1	vendor Specific

FIG. 5

TWO DIMENSIONAL ARRAY (MATRIX)

COLUMN NUMBERS

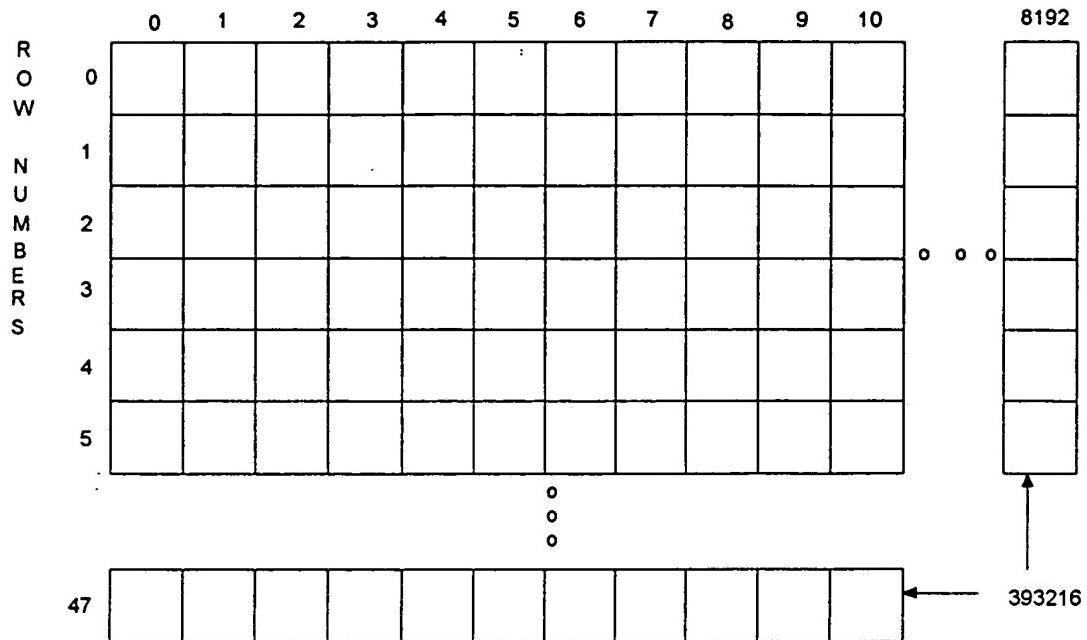
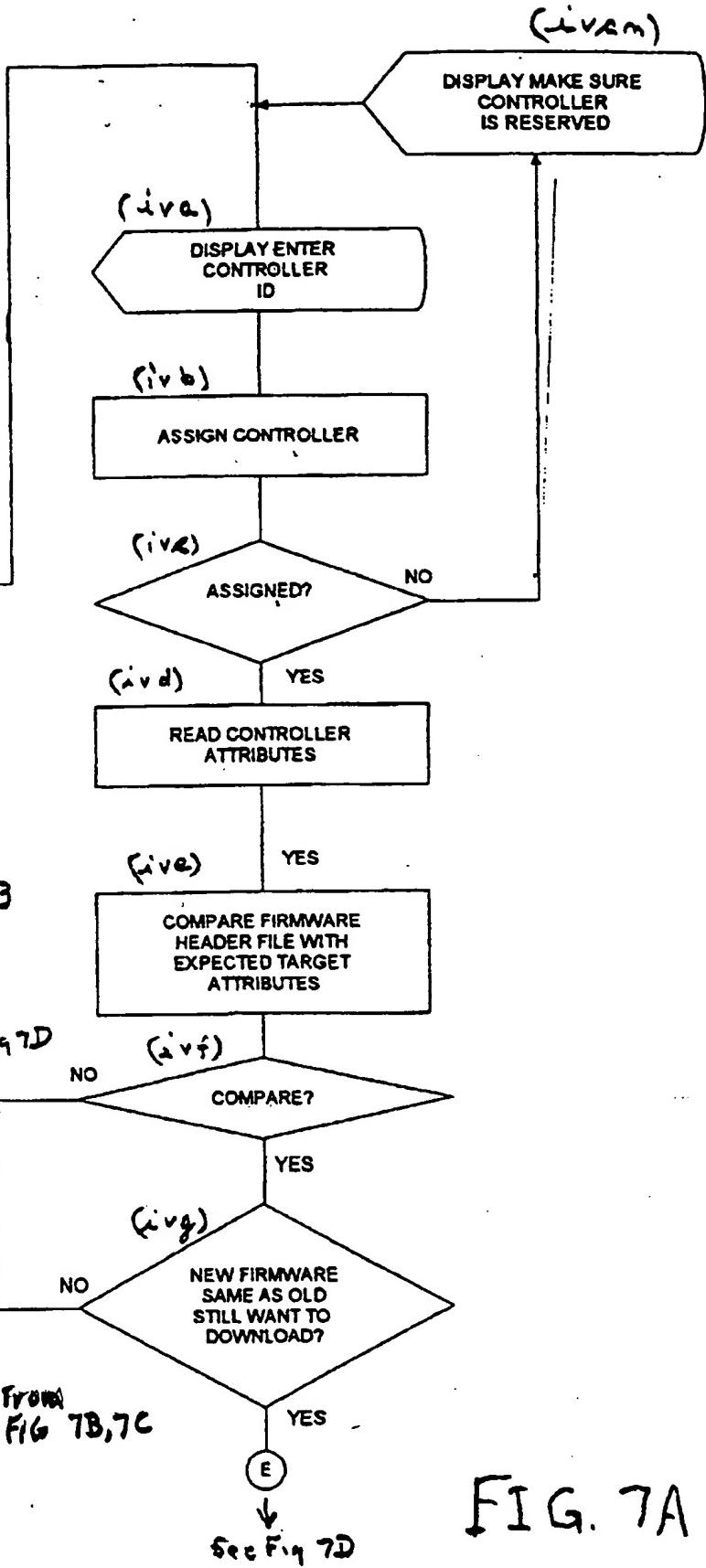
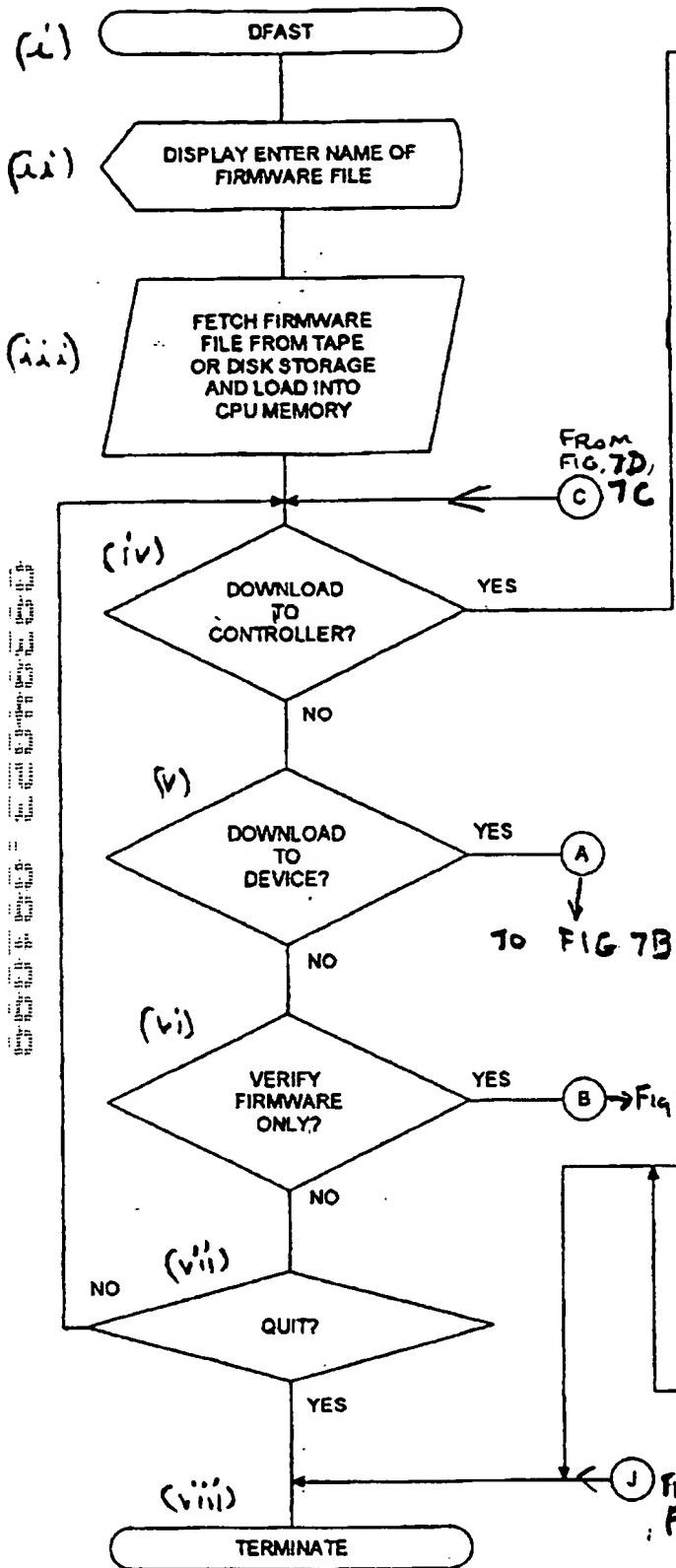
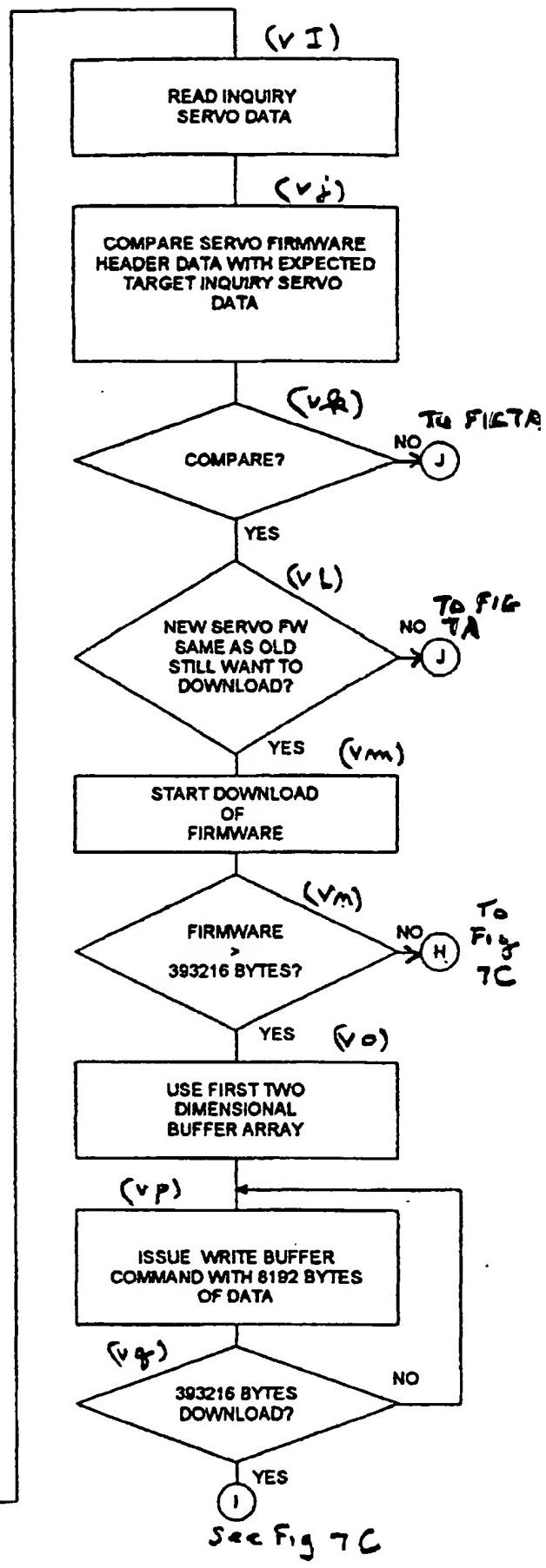
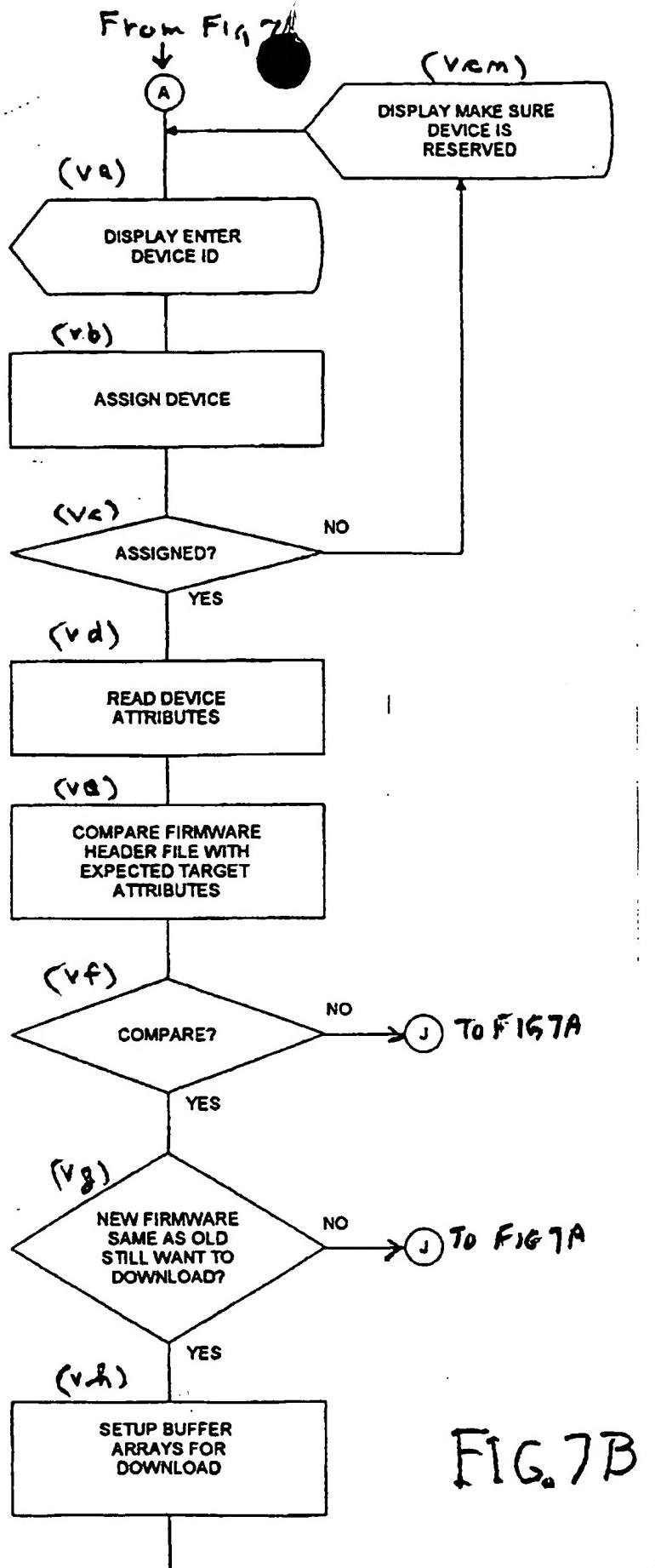


FIG. 6

ARRAY BUFFER [0:47, 0:8192]

DFAST





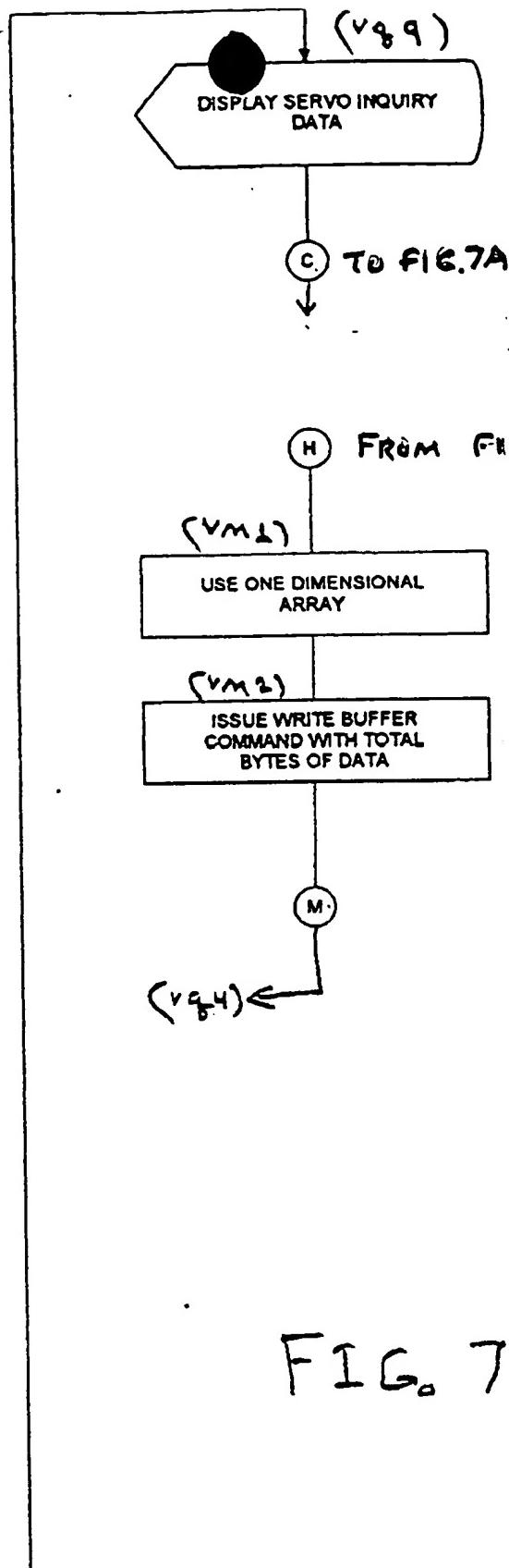
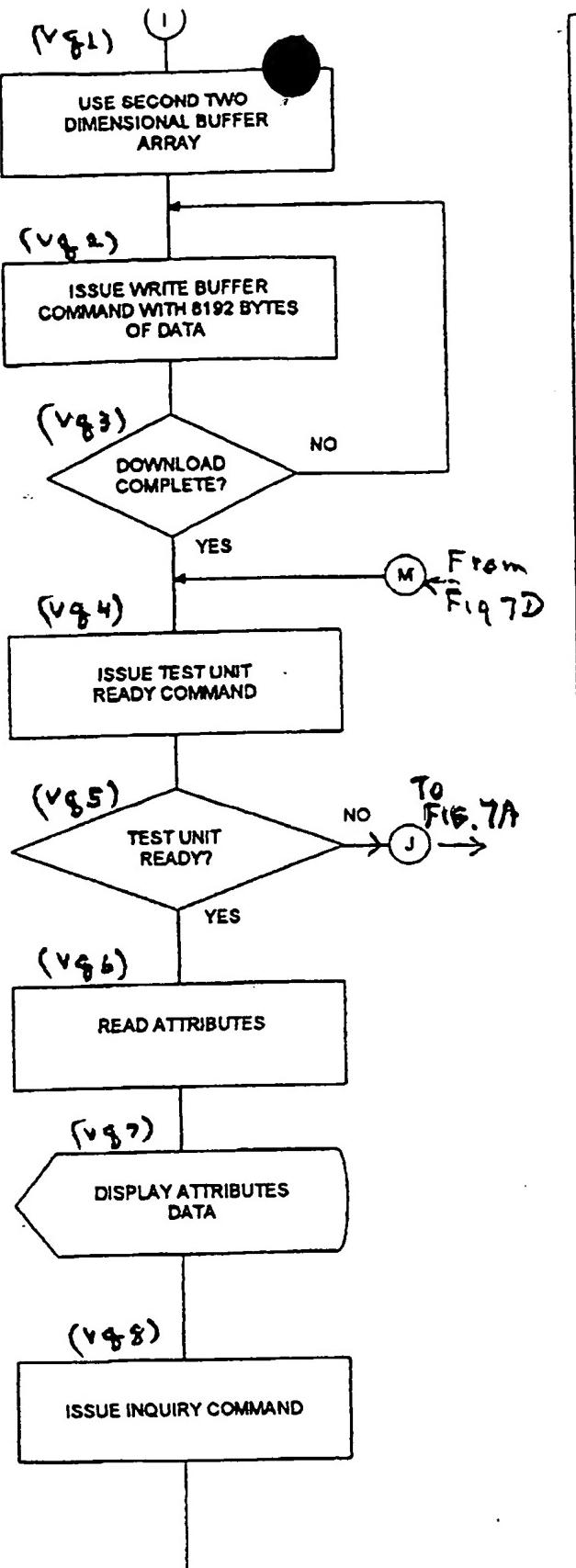
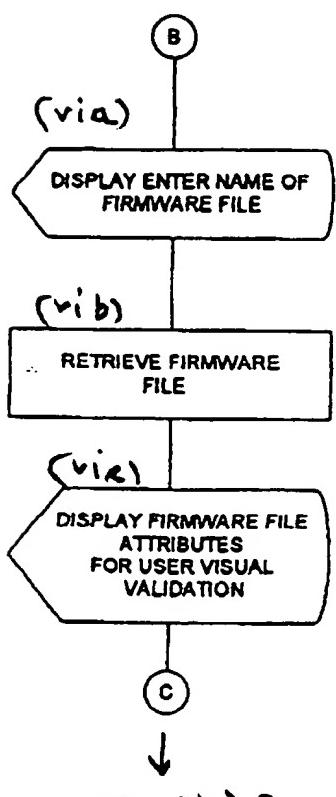


FIG. 7C

From Fig 7A



To (iv) Fig 7A

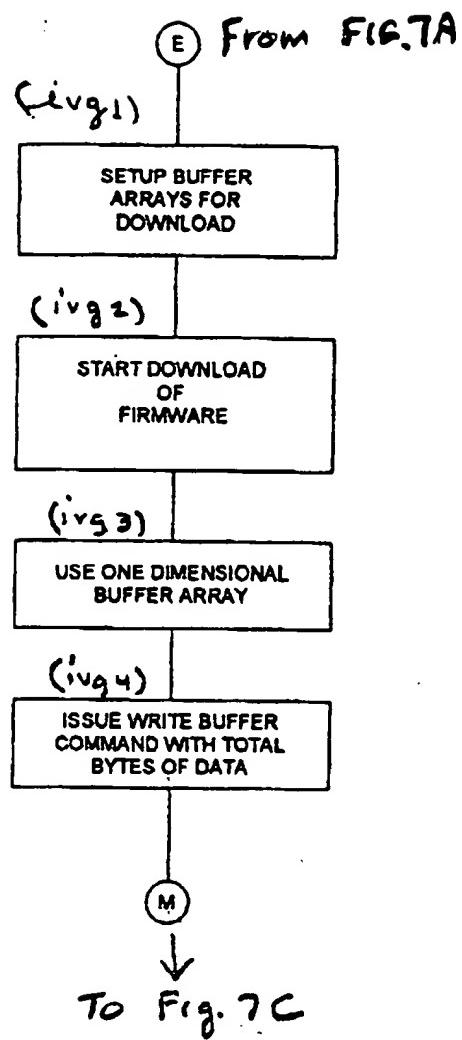


FIG. 7D